

Product Change Notice - PCN-2010-003

Rev	Date/Signature	Changes
1	11.10.2010/Sr	First Revision

1 Product affected

Product affected	Replacement
DIMM-SH7723-4	DIMM-SH7723-5
DIMM-SH7723-4-ET	DIMM-SH7723-5-ET
DIMM-SH7723-4-HL	DIMM-SH7723-5-HL

2 Change 1 – RCLK Clock

2.1 Key Characteristics of the Change

RCLK clock dropouts eliminated

2.2 Description of Change to the Customer

Because of dropouts of the RCLK clock the layout around the real time clock is improved.

2.3 Customer Impact of Change and Recommended Action

No actions necessary.

3 Change 2 – Watchdog

3.1 Key Characteristics of the Change

Reset topology changed

3.2 Description of Change to the Customer

Because of deadlocks of the NOR-Flash at watchdog events the topology of the reset signal is improved.

The reset signal is now routed through the reset module of the SH7723 CPU. All reset sources are connected to the RESETP# input pin of the CPU. The RESETOUT# pin of the CPU is now used to reset all chips (including NOR-Flash) on the CPU module and routed via a line buffer to the base board.

3.3 Customer Impact of Change and Recommended Action

An update of the uboot is necessary.

Please ask emtrion for the current software versions.

4 Change 3 – Production & Testability

4.1 Key Characteristics of the Change

Improvement of testability and production

4.2 Description of Change to the Customer

To improve the testability and production some test points are moved.

4.3 Customer Impact of Change and Recommended Action

No actions necessary.