

emtron GmbH
 Alter Schlachthof 45
 D-76131 Karlsruhe
 Tel. +49 (0) 721 62725-0
 Fax +49 (0) 721 62725-19
 E-Mail sales@emtron.com
 Visit us online at: www.emtron.com



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emSTAMP-Neon



emSTAMP-Neon

with STMicroelectronics STM32F4 Cortex-M4 controller

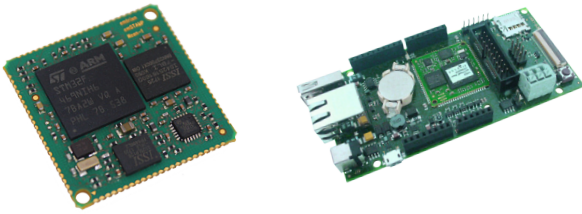
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embedded systems



Powered by **goal** Technology

emSTAMP-Neon

| | emSTAMP-Neon | SBC |
|-------------------------------------|--|--------------------------------|
| |  | |
| Product | emSTAMP-Neon | emSBC-Neon |
| Operating system | FreeRTOS | |
| Linux | on request | |
| CANopen Master/Slave | yes | |
| EtherNet/IP | yes | |
| DLR („Device Level Ring“) | yes | |
| EtherCAT | coming soon | |
| PROFINET CCB RT1 | yes | |
| Industrial Communication Middleware | GOAL by Port | |
| CPU manufacturer | STMicroelectronics | |
| CPU architecture | ARM Cortex-M4 | |
| CPU performance | 180 MHz - 225 DMIPS | |
| CPU | STM32F469 | |
| Graphics | Chrom-Art Accelerator (DMA 2D) | |
| RAM | up to external 32 MByte SDRAM, internal 384 KByte | |
| Flash | up to 2 MByte internal Flash, QSPI 16 MByte external | |
| Ethernet | 1 x 100 Base-TX | |
| UART | 2 x LVTTTL | |
| USB Host | 2.0 full speed | |
| CAN | 1 x LVTTTL | 1 x with transceiver |
| I ² C | 1 x | |
| SAI Audio | 1 x | |
| SPI | 2 x | |
| MMC/SDC | 1 x MMC/SD (SDIO) | 1 x micro SD |
| RTC | yes | 1 x battery holder with CR1632 |
| LCD DSI | 2 data lanes 24-bit RGB | - |
| LCD | RGB 18-bit | |
| Input (ADC) | 3 x | |
| Output (DAC) | 2 x | |
| Reset (In) | yes | |
| GPIO | 6 x | |
| PWM | 6 x complementary PWM outputs | |
| JTAG | yes | |
| Wake Up (IN) | yes | |
| PowerOut | - | +5V, +3V3 |
| Size in mm | 28 x 28 x 2,5 | 118 x 54 x 18 |
| max. power consumption | 0,36A @ 3V3 | 10W @ 7V - 12V |
| Operating temperature | 0...+70°C, opt. -40...+85°C | |
| Customer specific solutions | yes | |
| easy layout integration | 1,0 mm I/O-Pitch | - |



Subject to change without notice.
Rev1:37/2018

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