

PCN_DIMM-AM3354-512-512_v001en

Rev	Date/Signature	Changes
1	13.11.2013/Bue	First Revision

1 Product affected

Product affected	No.	Rev.	Replacement	Rev.
DIMM-AMX3354-512-512		R2B	DIMM-AMX3354-512-512	R2C

2 Change 1 – Processor

2.1 Key Characteristics of the Change

The processor AM3354ZCZD72 is replaced by the new 'B' type AM3354BZCZA80.

2.2 Description of Change to the Customer

The replacement of the processor means that the internal ROM code has now silicon revision 2.1. The document SPRZ360E from Texas Instruments lists the differences of the silicon revisions.

Also the maximum allowed CPU clock is now 800 MHz.

2.3 Customer Impact of Change and Recommended Action

The new processor is supported by U-Boot v003 for DIMM-AM335x