

Errata DIMM-MX257-2

Rev	Date/Signature	Changes
1	05.09.2011/Mt	First Revision

1 Product affected

Product affected	Rev.
DIMM-MX257-2	all R2 boards
DIMM-MX257-2 ET	all R2 boards
DIMM-MX257-2 CAM	all R2 boards
DIMM-MX257-2 CAM ET	all R2 boards

2 Errata 1 – UART1 RTS/CTS

2.1 Description

The signals UART1_RTS# and UART1_CTS# can't be used on all DIMM-MX257 layout revision R2.

2.2 Projected Impact

It isn't allowed to use the flow control of UART1, because the RTS and CTS signals are switched on the DIMM-MX257 boards. If the low control is used on UART1, RTS and CTS can be shorted. That could damage the boards.

2.3 Workaround

Use UART1 without a flow control.

2.4 Proposed Solution

This issue will be fixed at DIMM-MX257 boards with layout revision R3.