



Blockdiagram COM-RZ/N1D, v001en, 03.01.17

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	RZ/N1D-SBC	Flex2COM
		
Linux	yes	
CANopen Master/Slave	on request	
EtherNet/IP	on request	
DLR („Device Level Ring“)	on request	
EtherCAT	on request	
PROFINET CCB RT1	on request	
Industrial Communication Middleware	on request	
CPU manufacturer	Renesas RZ/N1D	
CPU architecture	ARM Cortex-A7 (dual core)	
CPU performance	2x 500 MHz	
Co-Processor	Cortex-M3 @ 125MHz	
RAM	512 MB DDR3-SDRAM	
Flash	8 GB NAND Flash	
Ethernet	2x 100 Base TX / 2x 1000 Base TX	
UART: RS232/RS485	1/2	
CAN	2 x 2.0 A/B	
RTC (battery buffered)	yes	
Size in mm	110 x 90 x 15	120 x 100 x 35
max. power consumption	10...30V / 200mA @ 24V	
Operating temperature	0...+70°C, opt. -40...+85°C	
Mounting	-	t-rail
CE Marking	-	yes
Emmission	-	EN 55011/CISPR11; Class A
EMC	-	EN 61000-6-2
Signaling	4 LEDs, protocol-specific	

Subject to change without notice.
Rev1:07/2019



emtrion GmbH
Alter Schlachthof 45
D-76131 Karlsruhe
Tel. +49 (0) 721 62725-0
Fax +49 (0) 721 62725-19
E-Mail sales@emtrion.com

Visit us online at:
www.emtrion.de