

Errata Verno-3

Rev	Date/Signature	Changes
1	11.07.2012/Mt	First Revision

1 Product affected

Product affected	Rev.
DIMM-EcoBase Verno	R3B

2 Errata 1 – I²C at Display connector J1

2.1 Description

The I²C interface at the display connector J1 doesn't work, because the SCL and SDA signals are switched at J1. Normally SCL must be at pin 4 and SDA at pin 3 as described in the Manuals, but on the boards it is different.

2.2 Projected Impact

The I²C interface at J1 is not available on DIMM-EcoBase Verno R3B.

2.3 Workaround

Switch the signals at the display cable.

2.4 Proposed Solution

This issue will be fixed at DIMM-EcoBase Verno R4A boards.