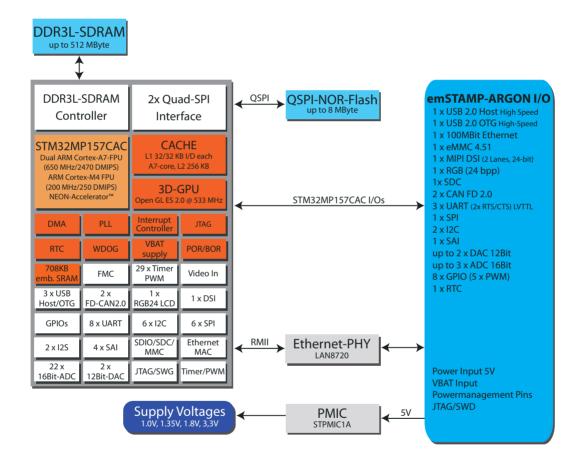


# emSTAMP-Argon based on STM32MP157CAC







### **Getting started with your embedded development!**

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## emSTAMP-Argon

|                                       | emSTAMP-Argon   | emSBC-Argon                    |
|---------------------------------------|---|--------------------------------|
|                                       |   |                                |
| Operatingsystem                       | Linux   |                                |
| CANopen Master/Slave                  | yes   |                                |
| CPU manufacturer                      | STMicroelectronics  |                                |
| CPU                                   | STM32MP157CAC   |                                |
| CPU architecture                      | ARM Dual Cortex-A7, ARM Cortex-M4   |                                |
| CPU performance                       | up to 2 x650 MHz - 2470 DMIPS + up to 200 MHz - 250 DMIPS   |                                |
| Co-Processor                          | ARM Neon™, FPU  |                                |
| 3D-Hardware GPU                       | OpenGL ES2.0 @ 533 MHz  |                                |
| Security                              | Crypto, TRNG, tamper, secure boot, Trust Zone   |                                |
| RAM                                   | up to 512 MByte DDR3L SDRAM   |                                |
| NOR-Flash                             | up to 8 MByte QSPI-NOR-Flash  |                                |
| eMMC 4.51                             | - '   | 4-64 GByte                     |
| I <sup>2</sup> C                      | 2   | .x                             |
| Ethernet                              | 1x 100MBit  |                                |
| SPI                                   | 1 x   |                                |
| USB 2.0 (High Speed)                  | 1 x Host, 1 x OTG   |                                |
| CAN FD 2.0                            | 2 x   |                                |
| UART                                  | 3 x (2 x RTS/CTS)   |                                |
| GPIO                                  | 8 x (5 x PWM)   |                                |
| LCD DSI                               | 2 data lanes 24-Bit   |                                |
| LCD RGB                               | 1 x 24 Bit  | -                              |
| SAI                                   | 1 x   |                                |
| ADC 16Bit                             | up to 3 x   |                                |
| DAC 12Bit                             | up to 2 x   |                                |
| LED                                   | green / red   |                                |
| MMC / SDC                             | 1 x SDC   | 1 x Pinheader or 1 x μSD-Card  |
| Debug                                 | SWD/JTAG  | -                              |
| RTC-battery-supply                    | 1 x   | 1 x battery holder with CR1225 |
| Power Management<br>(PowerDown, Wake) | yes   |                                |
| Operating temperature                 | -40°C +85°C   | 0°C +70°C                      |
| Size in mm                            | 32,0 x 32,0 x 4,0   | 104,5 x 77,5                   |
| Customer specific solutions           | 1 x 1 x battery holder with CR1225  yes  -40°C +85°C 0°C +70°C  32,0 x 32,0 x 4,0 104,5 x 77,5  yes  1,0mm I/O-Pitch (all Finepitch on CoM) |                                |
| easy layout integration               | 1,0mm I/O-Pitch (all Finepitch on CoM)  |                                |



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