

emSTAMP-Neon

with STMicroelectronics STM32F4 Cortex-M4 controller









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emSTAMP-Neon

OperatingsystemFreeRTOSLinuxon requestCANopen Master/SlaveyesEtherNet/IPyesDLR (Device Level Ring*)yesEtherAtTComing soonPROFINET CCB RT1yesIndustrial CommunicationGOAL by PortMiddlewareGOAL by PortCPU anchitectureARM Cortex-M4CPU performance1180 MHz- 225 DMIPSCPUSTM32F469RAMup to external 64 MByte SDRAM, internal 348 KByteFlashlup to 2 MByte internal Flash, OSPIGraphicsChrom-Art Accelerator (DMA 2D)USB 2.01 x HXCPC1 x RFis1 x RFis2 data lanes 2+bit RGBFis2 data lanes 2+bit RGB		emSTAMP-Neon	
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SD-Card 1 x LCD DSI 2 data lanes 24-bit RGB	SPI	2 x	
LCD DSI 2 data lanes 24-bit RGB	SD-Card	1 x	
	LCD DSI	2 data lanes 24-bit RGB	
	LCD	RGB 18-bit	
Input (ADC) 3 x	Input (ADC)	3 х	
Output (DAC) 2 x	Output (DAC)	2 x	ai
Reset (In) yes	Reset (In)	yes	notic
GPIU 6X	GPIO	6 X	hout
TAG		o x complementary PWW outputs	e wit
Operating temperature $0 \pm 70^{\circ}$ opt $\pm 40 \pm 85^{\circ}$	Operating temperature	$y = -40 + 20^{\circ}$	hang 18
Customer specific solutions ves	Customer specific solutions	νρς	5/20 c
easy layout integration 1,0 mm I/O-Pitch	easy layout integration	1,0 mm I/O-Pitch	ev1::5







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